Address	Size	Read /Write	content type	register	description	b7 128	b6 64	b5 32	b4 16	b3 8	b2 4	b1 2	b0 1
0x7E00	1	R/W	Stable	IICC	I²C interface configuration (0=disable,1=100kHz, 2=400kHz, 3=1MHz)								
0x7E01	1	R/W	volatile	LCDC	LCD interface configuration	0	0	1	8B	2L	10D	0	0
0x7E02	2	R/W	volatile	SCIC	Serial interface configuration (databit, parity, bauds)	9B	PAR	ODD	<-BAUD RATE:BITS 13:0->				
0x7E04	1	R/W	volatile	SDC	SDCARD configuration (0=close, 1=initialise, 2=error)							ERR	ST
0x7E05	2	R/W	Stable	SPIC	SPI configuration and speed								
0x7E07	2	R/W	volatile	PC0	Start/stop process 0 by controling Program counter	<------ 16 BIT PROGRAM COUNTER ----->							
0x7E09	1	R/W	volatile	CLATCH	Clear latch, writing 1 to this register clear all latches, read always 0								CL
0x7E0A													
0x7E0A													
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