

BAE0911 REGISTER MAP - registers hi

Address	Size	Read /Write	content type	register	description	
0x7F00	2	R/W	volatile	ADC0	Value of latest adc conversion (10 bits)	10bit value
0x7F02	2	R/W	volatile	ADC1	Value of latest adc conversion (10 bits)	10bit value
0x7F04	2	R/W	volatile	ADC2	Value of latest adc conversion (10 bits)	10bit value
0x7F06	2	R/W	volatile	ADC3	Value of latest adc conversion (10 bits)	10bit value
0x7F08	2	R/W	volatile	ADC4	Value of latest adc conversion (10 bits)	10bit value
0x7F0A	2	R/W	volatile	ADC5	Value of latest adc conversion (10 bits)	10bit value
0x7F0C	2	R/W	volatile	ADC6	Value of latest adc conversion (10 bits)	10bit value
0x7F0E	2	R/W	volatile	ADC7	Value of latest adc conversion (10 bits)	10bit value
0x7F10	2	R/W	volatile	ADC8	Value of latest adc conversion (10 bits)	10bit value
0x7F12	2	R/W	volatile	ADC9	Value of latest adc conversion (10 bits)	10bit value
0x7F14	2	R/W	volatile	ADC10	Value of latest adc conversion (10 bits)	10bit value
0x7F16	2	R/W	volatile	ADC11	Value of latest adc conversion (10 bits)	10bit value
0x7F18	2	R/W	volatile	ADC12	Value of latest adc conversion (10 bits)	10bit value
0x7F1A	2	R/W	volatile	ADC13	Value of latest adc conversion (10 bits)	10bit value
0x7F1C	2	R/W	volatile	ADC14	Value of latest adc conversion (10 bits)	10bit value
0x7F1E	2	R/W	volatile	ADC15	Value of latest adc conversion (10 bits)	10bit value
0x7F20	4	R/W	volatile	COUNT0	increments when edges detected on PIO0	DWORD value(32bit)
0x7F24	4	R/W	volatile	COUNT1	increments when edges detected on PIO1	DWORD value(32bit)
0x7F28	4	R/W	volatile	COUNT2	increments when edges detected on PIO2	DWORD value(32bit)
0x7F2C	4	R/W	volatile	COUNT3	increments when edges detected on PIO3	DWORD value(32bit)
0x7F30	4	R/W	volatile	COUNT4	increments when edges detected on PIO4	DWORD value(32bit)
0x7F34	4	R/W	volatile	COUNT5	increments when edges detected on PIO5	DWORD value(32bit)
0x7F38	4	R/W	volatile	COUNT6	increments when edges detected on PIO6	DWORD value(32bit)
0x7F3C	4	R/W	volatile	COUNT7	increments when edges detected on PIO7	DWORD value(32bit)
0x7F40	4	R/W	volatile	COUNT8	increments when edges detected on PIO8	DWORD value(32bit)
0x7F44	4	R/W	volatile	COUNT9	increments when edges detected on PIO9	DWORD value(32bit)
0x7F48	4	R/W	volatile	COUNT10	increments when edges detected on PIO10	DWORD value(32bit)
0x7F4C	4	R/W	volatile	COUNT11	increments when edges detected on PIO11	DWORD value(32bit)
0x7F50	4	R/W	volatile	COUNT12	increments when edges detected on PIO12	DWORD value(32bit)
0x7F54	4	R/W	volatile	COUNT13	increments when edges detected on PIO13	DWORD value(32bit)
0x7F58	4	R/W	volatile	COUNT14	increments when edges detected on PIO14	DWORD value(32bit)
0x7F5C	4	R/W	volatile	COUNT15	increments when edges detected on PIO15	DWORD value(32bit)
0x7F60	4	R/W	volatile	COUNT16	increments when edges detected on PIO16	DWORD value(32bit)
0x7F64	4	R/W	volatile	COUNT17	increments when edges detected on PIO17	DWORD value(32bit)
0x7F68	4	R/W	volatile	COUNT18	reserved	DWORD value(32bit)
0x7F6C	4	R/W	volatile	COUNT19	reserved	DWORD value(32bit)
0x7F70	4	R/W	volatile	COUNT20	reserved	DWORD value(32bit)
0x7F74	4	R/W	volatile	COUNT21	reserved	DWORD value(32bit)
0x7F78	1	R/W	stable	EALARMC	global alarm enable register	8bit value
0x7F79	1	R/W	volatile	OWIDLETIME	increment each seconds up to 255, reset to zero when device selected	BYTE value
0x7F7A	4	R/W	volatile	RTCH	realtime counter, increments each seconds	DWORD value(32bit)
0x7F7E	1	R	volatile	RTCL	256th seconds of RTC	BYTE value
0x7F7F	2	R/W	volatile	TIMER1	When enabled, the timer increments on each TPM1 period	WORD value
0x7F81	2	R/W	volatile	TIMER2	When enabled, the timer increments on each TPM2 period	WORD value
0x7F83	4	R/W	volatile	UALARM	32 alarm bits for user needs in AE logic	32 bits
0x7F87	4	R/W	stable	UALARM_EN	Enable bits for user alarm bits	32 bits
0x7F8B	32	R/W	volatile	USER_ARRAY	User byte array- with 1Hz auto decrement on Ncountdown	array of byte [32]
0x7FAB	16	R/W	volatile	USER_BYTES	byte user register	BYTE[16] values
0x7FBB	32	R/W	volatile	USER_WORDS	word user register	WORD[16] values
0x7FDB	32	R/W	volatile	USER_WW	dword user register	DWORD[8] 32bit values
0x7FFB						
0x7FFB						
0x7FFB						