Address	Size	Read /Write	content type	register	description	b7 128	b6 64	b5 32	b4 16	b3 8	b2 4	b1 2	b0 1
0x7900	1	R/W	volatile	PIO0	set/read pio 0								D
0x7901	1	R/W	volatile	PIO1	set/read pio 1								D
0x7902	1	R/W	volatile	PIO2	set/read pio 2								D
0x7903	1	R/W	volatile	PIO3	set/read pio 3								D
0x7904	1	R/W	volatile	PIO4	set/read pio 4								D
0x7905	1	R/W	volatile	PIO5	set/read pio 5								D
0x7906	1	R/W	volatile	PIO6	set/read pio 6								D
0x7907	1	R/W	volatile	PIO7	set/read pio 7								D
0x7908	1	R/W	volatile	PIO8	set/read pio 8								D
0x7909	1	R/W	volatile	PIO9	set/read pio 9								D
0x790A	1	R/W	volatile	PIO10	set/read pio 10								D
0x790B	1	R/W	volatile	PIO11	set/read pio 11								D
0x790C	1	R/W	volatile	PIO12	set/read pio 12								D
0x790D	1	R/W	volatile	PIO13	set/read pio 13								D
0x790E	1	R/W	volatile	PIO14	set/read pio 14								D
0x790F	1	R/W	volatile	PIO15	set/read pio 15								D
0x7910	1	R/W	volatile	PIO16	set/read pio 16								D
0x7911	1	R/W	volatile	PIO17	set/read pio 17								D
0x7912	1	R/W	volatile	PIO18	set/read pio 18								D
0x7913	1	R/W	volatile	PIO19	set/read pio 19								D
0x7914	1	R/W	volatile	PIO20	set/read pio 20								D
0x7915	1	R/W	volatile	PIO21	set/read pio 21								D
0x7916	1	R/W	stable	PIODIR0	configure pin as input(0) or output(1) for pio 0								DD
0x7917	1	R/W	stable	PIODIR1	configure pin as input(0) or output(1) for pio 1								DD
0x7918	1	R/W	stable	PIODIR2	configure pin as input(0) or output(1) for pio 2								DD
0x7919	1	R/W	stable	PIODIR3	configure pin as input(0) or output(1) for pio 3								DD
0x791A	1	R/W	stable	PIODIR4	configure pin as input(0) or output(1) for pio 4								DD
0x791B	1	R/W	stable	PIODIR5	configure pin as input(0) or output(1) for pio 5								DD
0x791C	1	R/W	stable	PIODIR6	configure pin as input(0) or output(1) for pio 6								DD
0x791D	1	R/W	stable	PIODIR7	configure pin as input(0) or output(1) for pio 7								DD
0x791E	1	R/W	stable	PIODIR8	configure pin as input(0) or output(1) for pio 8								DD
0x791F	1	R/W	stable	PIODIR9	configure pin as input(0) or output(1) for pio 9								DD
0x7920	1	R/W	stable	PIODIR10	configure pin as input(0) or output(1) for pio 10								DD
0x7921	1	R/W	stable	PIODIR11	configure pin as input(0) or output(1) for pio 11								DD
0x7922	1	R/W	stable	PIODIR12	configure pin as input(0) or output(1) for pio 12								DD
0x7923	1	R/W	stable	PIODIR13	configure pin as input(0) or output(1) for pio 13								DD
0x7924	1	R/W	stable	PIODIR14	configure pin as input(0) or output(1) for pio 14								DD
0x7925	1	R/W	stable	PIODIR15	configure pin as input(0) or output(1) for pio 15								DD
0x7926	1	R/W	stable	PIODIR16	configure pin as input(0) or output(1) for pio 16								DD
0x7927	1	R/W	stable	PIODIR17	configure pin as input(0) or output(1) for pio 17								DD
0x7928	1	R/W	stable	PIODIR18	configure pin as input(0) or output(1) for pio 18								DD
0x7929	1	R/W	stable	PIODIR19	configure pin as input(0) or output(1) for pio 19								DD
0x792A	1	R/W	stable	PIODIR20	configure pin as input(0) or output(1) for pio 20								DD
0x792B	1	R/W	stable	PIODIR21	configure pin as input(0) or output(1) for pio 21								DD
0x792C	1	R/W	stable	PIODS0	Output strenght for pio 0								DS
0x792D	1	R/W	stable	PIODS1	Output strenght for pio 1								DS
0x792E	1	R/W	stable	PIODS2	Output strenght for pio 2								DS
0x792F	1	R/W	stable	PIODS3	Output strenght for pio 3								DS
0x7930	1	R/W	stable	PIODS4	Output strenght for pio 4								DS
0x7931	1	R/W	stable	PIODS5	Output strenght for pio 5								DS
0x7932	1	R/W	stable	PIODS6	Output strenght for pio 6								DS
0x7933	1	R/W	stable	PIODS7	Output strenght for pio 7								DS
0x7934	1	R/W	stable	PIODS8	Output strenght for pio 8								DS
0x7935	1	R/W	stable	PIODS9	Output strenght for pio 9								DS
0x7936	1	R/W	stable	PIODS10	Output strenght for pio 10								DS
0x7937	1	R/W	stable	PIODS11	Output strenght for pio 11								DS
0x7938	1	R/W	stable	PIODS12	Output strenght for pio 12								DS
0x7939	1	R/W	stable	PIODS13	Output strenght for pio 13								DS
0x793A	1	R/W	stable	PIODS14	Output strenght for pio 14								DS
0x793B	1	R/W	stable	PIODS15	Output strenght for pio 15								DS

Address	Size	Read /Write	content type	register	description	b7 128	b6 64	b5 32	b4 16	b3 8	b2 4	b1 2	b0 1
0x793C	1	R/W	stable	PIODS16	Output strenght for pio 16								DS
0x793D	1	R/W	stable	PIODS17	Output strenght for pio 17								DS
0x793E	1	R/W	stable	PIODS18	Output strenght for pio 18								DS
0x793F	1	R/W	stable	PIODS19	Output strenght for pio 19								DS
0x7940	1	R/W	stable	PIODS20	Output strenght for pio 20								DS
0x7941	1	R/W	stable	PIODS21	Output strenght for pio 21								DS
0x7942	1	R/W	stable	PIOPE0	Enable Pullup (down) resitor on input for pio 0								PE
0x7943	1	R/W	stable	PIOPE1	Enable Pullup (down) resitor on input for pio 1								PE
0x7944	1	R/W	stable	PIOPE2	Enable Pullup (down) resitor on input for pio 2								PE
0x7945	1	R/W	stable	PIOPE3	Enable Pullup (down) resitor on input for pio 3								PE
0x7946	1	R/W	stable	PIOPE4	Enable Pullup (down) resitor on input for pio 4								PE
0x7947	1	R/W	stable	PIOPE5	Enable Pullup (down) resitor on input for pio 5								PE
0x7948	1	R/W	stable	PIOPE6	Enable Pullup (down) resitor on input for pio 6								PE
0x7949	1	R/W	stable	PIOPE7	Enable Pullup (down) resitor on input for pio 7								PE
0x794A	1	R/W	stable	PIOPE8	Enable Pullup resitor on input for pio 8								PE
0x794B	1	R/W	stable	PIOPE9	Enable Pullup resitor on input for pio 9								PE
0x794C	1	R/W	stable	PIOPE10	Enable Pullup resitor on input for pio 10								PE
0x794D	1	R/W	stable	PIOPE11	Enable Pullup resitor on input for pio 11								PE
0x794E	1	R/W	stable	PIOPE12	Enable Pullup resitor on input for pio 12								PE
0x794F	1	R/W	stable	PIOPE13	Enable Pullup resitor on input for pio 13								PE
0x7950	1	R/W	stable	PIOPE14	Enable Pullup resitor on input for pio 14								PE
0x7951	1	R/W	stable	PIOPE15	Enable Pullup resitor on input for pio 15								PE
0x7952	1	R/W	stable	PIOPE16	Enable Pullup resitor on input for pio 16								PE
0x7953	1	R/W	stable	PIOPE17	Enable Pullup resitor on input for pio 17								PE
0x7954	1	R/W	stable	PIOPE18	Enable Pullup resitor on input for pio 18								PE
0x7955	1	R/W	stable	PIOPE19	Enable Pullup resitor on input for pio 19								PE
0x7956	1	R/W	stable	PIOPE20	Enable Pullup resitor on input for pio 20								PE
0x7957	1	R/W	stable	PIOPE21	Enable Pullup resitor on input for pio 21								PE
0x7958	1	R/W	volatile	LATCH0	The latch bit state track changes on pio 0								LTCH
0x7959	1	R/W	volatile	LATCH1	The latch bit state track changes on pio 1								LTCH
0x795A	1	R/W	volatile	LATCH2	The latch bit state track changes on pio 2								LTCH
0x795B	1	R/W	volatile	LATCH3	The latch bit state track changes on pio 3								LTCH
0x795C	1	R/W	volatile	LATCH4	The latch bit state track changes on pio 4								LTCH
0x795D	1	R/W	volatile	LATCH5	The latch bit state track changes on pio 5								LTCH
0x795E	1	R/W	volatile	LATCH6	The latch bit state track changes on pio 6								LTCH
0x795F	1	R/W	volatile	LATCH7	The latch bit state track changes on pio 7								LTCH
0x7960	1	R/W	volatile	LATCH8	The latch bit state track changes on pio 8								LTCH
0x7961	1	R/W	volatile	LATCH9	The latch bit state track changes on pio 9								LTCH
0x7962	1	R/W	volatile	LATCH10	The latch bit state track changes on pio 10								LTCH
0x7963	1	R/W	volatile	LATCH11	The latch bit state track changes on pio 11								LTCH
0x7964	1	R/W	volatile	LATCH12	The latch bit state track changes on pio 12								LTCH
0x7965	1	R/W	volatile	LATCH13	The latch bit state track changes on pio 13								LTCH
0x7966	1	R/W	volatile	LATCH14	The latch bit state track changes on pio 14								LTCH
0x7967	1	R/W	volatile	LATCH15	The latch bit state track changes on pio 15								LTCH
0x7968	1	R/W	volatile	LATCH16	The latch bit state track changes on pio 16								LTCH
0x7969	1	R/W	volatile	LATCH17	The latch bit state track changes on pio 17								LTCH
0x796A	1	R/W	volatile	LATCH18	The latch bit state track changes on pio 18								LTCH
0x796B	1	R/W	volatile	LATCH19	The latch bit state track changes on pio 19								LTCH
0x796C	1	R/W	volatile	LATCH20	The latch bit state track changes on pio 20								LTCH
0x796D	1	R/W	volatile	LATCH21	The latch bit state track changes on pio 21								LTCH
0x796E	1	R/W	stable	ECOUNT0	Enable 32bit counter of falling edges of pio 0								EC
0x796F	1	R/W	stable	ECOUNT1	Enable 32bit counter of falling edges of pio 1								EC
0x7970	1	R/W	stable	ECOUNT2	Enable 32bit counter of falling edges of pio 2								EC
0x7971	1	R/W	stable	ECOUNT3	Enable 32bit counter of falling edges of pio 3								EC
0x7972	1	R/W	stable	ECOUNT4	Enable 32bit counter of falling edges of pio 4								EC
0x7973	1	R/W	stable	ECOUNT5	Enable 32bit counter of falling edges of pio 5								EC
0x7974	1	R/W	stable	ECOUNT6	Enable 32bit counter of falling edges of pio 6								EC
0x7975	1	R/W	stable	ECOUNT7	Enable 32bit counter of falling edges of pio 7								EC
0x7976	1	R/W	stable	ECOUNT8	Enable 32bit counter of falling edges of pio 8								EC
0x7977	1	R/W	stable	ECOUNT9	Enable 32bit counter of falling edges of pio 9								EC

Address	Size	Read /Write	content type	register	description	b7 128	b6 64	b5 32	b4 16	b3 8	b2 4	b1 2	b0 1
0x7978	1	R/W	stable	ECOUNT10	Enable 32bit counter of falling edges of pio 10								EC
0x7979	1	R/W	stable	ECOUNT11	Enable 32bit counter of falling edges of pio 11								EC
0x797A	1	R/W	stable	ECOUNT12	Enable 32bit counter of falling edges of pio 12								EC
0x797B	1	R/W	stable	ECOUNT13	Enable 32bit counter of falling edges of pio 13								EC
0x797C	1	R/W	stable	ECOUNT14	Enable 32bit counter of falling edges of pio 14								EC
0x797D	1	R/W	stable	ECOUNT15	Enable 32bit counter of falling edges of pio 15								EC
0x797E	1	R/W	stable	ECOUNT16	Enable 32bit counter of falling edges of pio 16								EC
0x797F	1	R/W	stable	ECOUNT17	Enable 32bit counter of falling edges of pio 17								EC
0x7980	1	R/W	stable	ECOUNT18	reserved								EC
0x7981	1	R/W	stable	ECOUNT19	reserved								EC
0x7982	1	R/W	stable	ECOUNT20	reserved								EC
0x7983	1	R/W	stable	ECOUNT21	reserved								EC
0x7984	1	R/W	stable	ALARM0	Alarm enable on latch of pio 0								ALRM
0x7985	1	R/W	stable	ALARM1	Alarm enable on latch of pio 1								ALRM
0x7986	1	R/W	stable	ALARM2	Alarm enable on latch of pio 2								ALRM
0x7987	1	R/W	stable	ALARM3	Alarm enable on latch of pio 3								ALRM
0x7988	1	R/W	stable	ALARM4	Alarm enable on latch of pio 4								ALRM
0x7989	1	R/W	stable	ALARM5	Alarm enable on latch of pio 5								ALRM
0x798A	1	R/W	stable	ALARM6	Alarm enable on latch of pio 6								ALRM
0x798B	1	R/W	stable	ALARM7	Alarm enable on latch of pio 7								ALRM
0x798C	1	R/W	stable	ALARM8	Alarm enable on latch of pio 8								ALRM
0x798D	1	R/W	stable	ALARM9	Alarm enable on latch of pio 9								ALRM
0x798E	1	R/W	stable	ALARM10	Alarm enable on latch of pio 10								ALRM
0x798F	1	R/W	stable	ALARM11	Alarm enable on latch of pio 11								ALRM
0x7990	1	R/W	stable	ALARM12	Alarm enable on latch of pio 12								ALRM
0x7991	1	R/W	stable	ALARM13	Alarm enable on latch of pio 13								ALRM
0x7992	1	R/W	stable	ALARM14	Alarm enable on latch of pio 14								ALRM
0x7993	1	R/W	stable	ALARM15	Alarm enable on latch of pio 15								ALRM
0x7994	1	R/W	stable	ALARM16	Alarm enable on latch of pio 16								ALRM
0x7995	1	R/W	stable	ALARM17	Alarm enable on latch of pio 17								ALRM
0x7996	1	R/W	stable	ALARM18	Alarm enable on latch of pio 18								ALRM
0x7997	1	R/W	stable	ALARM19	Alarm enable on latch of pio 19								ALRM
0x7998	1	R/W	stable	ALARM20	Alarm enable on latch of pio 20								ALRM
0x7999	1	R/W	stable	ALARM21	Alarm enable on latch of pio 21								ALRM
0x799A	1	R/W	stable	EENCODER0	Enable encoder 0								EE
0x799B	1	R/W	stable	EENCODER5	Enable encoder 5								EE
0x799C	1	R/W	stable	EENCODER8	Enable encoder 8								EE
0x799D	1	R/W	stable	EENCODER9	Enable encoder 9								EE
0x799E	1	R/W	stable	EENCODER16	Enable encoder 16								EE
0x799F	1	R/W	stable	EENCODER17	Enable encoder 17								EE
0x79A0	1	R/W	stable	PULLDOWN0	Pulldown (require pull enable bit) on pio 0								U
0x79A1	1	R/W	stable	PULLDOWN1	Pulldown (require pull enable bit) on pio 1								U
0x79A2	1	R/W	stable	PULLDOWN2	Pulldown (require pull enable bit) on pio 2								U
0x79A3	1	R/W	stable	PULLDOWN3	Pulldown (require pull enable bit) on pio 3								U
0x79A4	1	R/W	stable	PULLDOWN4	Pulldown (require pull enable bit) on pio 4								U
0x79A5	1	R/W	stable	PULLDOWN5	Pulldown (require pull enable bit) on pio 5								U
0x79A6	1	R/W	stable	PULLDOWN6	Pulldown (require pull enable bit) on pio 6								U
0x79A7	1	R/W	stable	PULLDOWN7	Pulldown (require pull enable bit) on pio 7								U
0x79A8	1	R/W	volatile	CAPFLAG1C0	Capture bit for of pwm 1C0								CF
0x79A9	1	R/W	volatile	CAPFLAG1C1	Capture bit for of pwm 1C1								CF
0x79AA	1	R/W	volatile	CAPFLAG2C0	Capture bit for of pwm 2C0								CF
0x79AB	1	R/W	volatile	CAPFLAG2C1	Capture bit for of pwm 2C1								CF
0x79AC	1	R/W	stable	ACMPE	Analog comparator enable								
0x79AD	1	R/W	stable	ACMPIS	Analog comparator fixed 1.2V internal source for ACMP+								
0x79AE	1	R/W	volatile	ACMPLATCH	Analog comparator event occured LATCH								
0x79AF	1	R/W	volatile	ACMPO	Analog comparator output								
0x79B0	1	R/W	stable	ACMPOE	Analog comparator enable output on pio 2								