

BAE0911 - PIO MAPPING

MC9S08SH	PIN	FUNCTION	ALT1	ALT2	ALT3	ALT4
PTA5	3	DQ_IN				
PTA4	4	DQ_OUT				
GND	8	GND				
GNDA	7	GND				
PTA0	26	PIO00	ADC00	COUNT00	* INCR00	ACMP+
PTA1	25	PIO01	ADC01	COUNT01	DECR00	ACMP-
PTA2	24	PIO02	ADC02	COUNT02		ACMPO
PTA3	23	PIO03	ADC03	COUNT03		
PTB0	20	PIO04	ADC04	COUNT04	DECR05	RXD
PTB1	19	PIO05	ADC05	COUNT05	* INCR05	TXD
PTB2	18	PIO06	ADC06	COUNT06		SPCLK
PTB3	17	PIO07	ADC07	COUNT07		MOSI
PTC0	16	PIO08	ADC08	COUNT08	* INCR08	PWM/CAPTURE 1C0
PTC1	15	PIO09	ADC09	COUNT09	* INCR09	PWM/CAPTURE 1C1
PTC2	14	PIO10	ADC10	COUNT10		
PTC3	13	PIO11	ADC11	COUNT11		
PTC4	2	PIO12	ADC12	COUNT12	DECR17	
PTC5	1	PIO13	ADC13	COUNT13	DECR16	
PTC6	28	PIO14	ADC14	COUNT14		
PTC7	27	PIO15	ADC15	COUNT15		
PTA6	22	PIO16		COUNT16	* INCR16	PWM/CAPTURE 2C0
PTA7	21	PIO17		COUNT17	* INCR17	PWM/CAPTURE 2C1
PTB4	12	PIO18				MISO
PTB5	11	PIO19				SS
PTB6	10	PIO20			DECR09	SDA
PTB7	9	PIO21			DECR08	SCL
VCC	5	VCC				
VCCA	6	VCC				

all counters are counting on falling edge with exception of *
 *counter/encoder with dedicated irq, allows counting on rising edge