BAE0911 REGISTER MAP - register byte mapped

Address	Size	Read /Write	content type	register	description	Register mask	b7 128	b6 64	b5 32	b4 16	b3 8	b2 4	b1 2	b0 1
0x7A00	1	R/W	Stable	TPM1C	TPM clock configuration for pwm1 Ch0 & Ch1	0b00011111	-	T1EN	-	CLOCK1 PRESCALE				
0x7A01	1	R/W	Stable	TPM2C	TPM clock configuration for pwm2 Ch0 & Ch1	0b00011111	-	T2EN	-	CLOCK2 PRESCALE				
0x7A02	2	R/W	Stable	PERIOD1	Period for pwm 1 ch0 & ch1	0xff	*MSB*							
0x7A03						0xff								*LSB*
0x7A04	2	R/W	Stable	PERIOD2	Period for pwm 2 ch0 & ch1	0xff	*MSB*							
0x7A05						0xff								*LSB*
0x7A06	2	R/W	Stable	DUTY1C0	Duty for pwm1 channel 0	0xff	*MSB*							
0x7A07						0xff								*LSB*
0x7A08	2	R/W	Stable	DUTY1C1	Duty for pwm1 channel 1	0xff	*MSB*							
0x7A09						0xff								*LSB*
0x7A0A	2	R/W	Stable	DUTY2C0	Duty for pwm2 channel 0	0xff	*MSB*							
0x7A0B						0xff								*LSB*
0x7A0C	2	R/W	Stable	DUTY2C1	Duty for pwm2 channel 1	0xff	*MSB*							
0x7A0D						0xff								*LSB*
0x7A0E	1	R/W	stable	C1CH0	Configuration for pwm/capture channel 1/0	0b00111100	FL	IE	MODE				-	-
0x7A0F	1	R/W	stable	C1CH1	Configuration for pwm/capture channel 1/1	0b00111100	FL	IE	MODE				-	-
0x7A10	1	R/W	stable	C2CH0	Configuration for pwm/capture channel 2/0	0b00111100	FL	IE	MODE				-	-
0x7A11	1	R/W	stable	C2CH1	Configuration for pwm/capture channel 2/0	0b00111100	FL	IE	MODE				-	-
0x7A12	1	R/W	Stable	NBYTES	Countdown array size	0b00011111				*up to 31 bytes*				
0x7A13														
0x7A14														
0x7A15														